



ASIC/FPGA Senior Engineer

Who We Are

Comcores is a leading provider of silicon intellectual property (SIP's) interface and signal processing solutions for wireless and wireline networks. We are a highly experienced team passionate about delivering unique solutions to world leading companies globally making ASIC or FPGA-based solutions for the communication market.

We strive to front new technologies that enables next generation wireless and wireline networks and are engaged in powerful research partnerships, standardization work and industry relations that can ensure our competitiveness and enable new commercial solutions.

We value a close relationship and a relaxing atmosphere and want to achieve great results.

The Open Position

Comcores is looking for a motivated and highly skilled colleague that wants to work with a leading provider of digital subsystem solutions for 5G wireless infrastructure and has experience designing protocols in VHDL.

Job Description

Your overall focus will be development and support of digital IP solutions for wireless infrastructure. You will be generating RTL, creating corresponding test plans and documentation, and applying your skills to support verification activities at IP, sub-system, and system/-architecture levels in order to ensure a robust experience for our customers.

You will have a particular focus on supporting and developing solutions for wireless fronthaul. The work will cover strategic product developments as well as customer driven projects.

You will be working closely with the rest of the Comcores team to ensure alignment with existing developments within the area as well as customers globally. When the designs are productized, you will be supporting customers to assure seamless integration in their projects.

Key Responsibilities

The tasks will span:

- Draft a design specification based on requirements flowed down from Product Management
- Develop RTL utilizing VHDL and debug the design via simulation tools
- Perform synthesis and place and route (FPGA) or support foundry backend development activities (ASIC)
- Work with an independent verification team to resolve bugs found in the design
- Documentation in form of a user manual for the design
- Support customers and in-house teams in integration of the product

Qualifications

You are an experienced VHDL design engineer and hold a Master's or Bachelor's engineering degree.

You are used to make digital projects for FPGA or ASIC.

Ideally you are familiar with wireless systems and protocols and are used to work with digital designs in Xilinx/Altera/Synopsys tools.

You are a team player by nature with the ability to work independently with a result-oriented 'can-do' attitude. You are comfortable working in a dynamic environment and have a constructive attitude to challenges occurring during your daily work.

Personally you are focused, systematic and solution oriented person that manage to transform your skills into action. You feel motivated by being the best within your field and like to work with the newest technologies.

Setting New Standards?

Do you want to work in a passionate and energetic organization being a first mover in several technologies areas and work in an environment characterized by great colleagues, flexibility and an informal atmosphere? Then we would very much like to hear from you.

Please send us your resume to job@comcores.com

For further information about the position please contact Thomas Nørgaard at +4527113480